

Jonathan Sapan

Patent Engineer

Jonathan Sapan is a patent engineer in the Intellectual Property Practice Group. He has a fundamental background in mathematics, solid state devices, digital logic design, and signal processing. Jonathan has worked as an electrical engineer and applied physicist in academia and industry. Prior to his graduate work, he worked on optical communications, optical pressure sensors, and high-bandwidth (0-40 GHz) radiofrequency/microwave (RF/MW) test and measurement at a leading corporate R&D division, and on biometric security systems and carbon nanotube electronics at startups.

As an academic researcher, Jonathan studied the nanoscale properties of magnetic thin films and devices, using magnetometry, x-ray scattering, and synchrotron x-ray spectroscopy, including published work on spin-torque MRAM (STT-MRAM) in collaboration with a multinational wireless and technology innovation corporation. Jonathan has hands-on experience in micro and nanofabrication, including conventional semiconductor fabrication techniques (optical and electron lithography, thin film deposition, wet and plasma processing, etc.) and next-generation technologies such as hard and soft nanoimprint lithography. He also has experience programming in C, Python, and Assembly languages and has experience working with distributed computing and big data technologies.

Immediately prior to joining Quarles & Brady, Jonathan helped prosecute patent applications for Fortune 500 clients in IT hardware/software and aerospace/defense, including working directly with inventors and drafting applications. At the firm, he works on prosecuting patents in diverse fields including optics, materials science, semiconductors, biomedical devices, aerospace and other mechanical inventions. He also works on software inventions in fields including cryptography, cloud computing, and wireless technology.

Jonathan is a Six Sigma Green Belt recipient and past member of the Princeton University Alumni Technology Advisory Committee. He enjoys literature, is a strong technical writer, and has taught undergraduate writing.

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Education and Honors



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- Princeton University (B.S.E., 2004)
 - Major: Electrical Engineering (solid state devices)
 - University of California at San Diego
 - Electrical Engineering (nanoscale devices and systems)
 - Malcom R. Stacey Memorial Scholarship
 - Honorable Mention, NSF Graduate Research Fellowship Program

Professional and Civic Activities

- Princeton University Alumni Council, Technology Advisory Committee Member, Volunteer Alumni Interviewer
- Six Sigma Green Belt
- Member of IEEE and American Physical Society